



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner PATENT APPLICATION  
Serial No.: 10/810,035 Group Art Unit: 2818  
Filed: March 26, 2004 Examiner: T. T. Nguyen  
Confirmation No.: 8458 Attorney Docket No.: ATM-291  
For: NON-VOLATILE TRANSISTOR MEMORY ARRAY  
INCORPORATING READ-ONLY ELEMENTS  
WITH SINGLE MASK SET

Amendment

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office action mailed 08/23/2005 regarding the above-identified patent application, Applicant responds as follows.

Amended and new claims start on page 2.  
Remarks start on page 9.  
Summary and conclusion start on page 10.

The originally filed claims were 1-19. Claims 11-19 were withdrawn in response to a restriction. Claims 1-10 are remaining. Claims 20-30 have been added.

03/01/2006 MBIZUNES 00000006 10810035

02 FC:1202	500.00 OP
03 FC:1201	400.00 OP